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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/036,789 12/21/2001		Gerald G. Pechanek	800.0119	5428	
27997 75	10/22/2003	EXAMINER		NER	
PRIEST & GOLDSTEIN PLLC 5015 SOUTHPARK DRIVE SUITE 230			ENG, DAVID Y		
			ART UNIT	PAPER NUMBER	
DURHAM, NC 27713-7736			2155		
			DATE MAILED: 10/22/2003	E MAILED: 10/22/2003 / O	

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)			
_	10/036,789	PECHANEK ET AL.			
Office Action Summary	Examiner	Art Unit			
	DAVID Y. ENG	2155			
The MAILING DATE of this communication app Period for Reply	ears on the cover she t with the c	orresp ndence address			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).  Status	within the statutory minimum of thirty (30) day ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
1) Responsive to communication(s) filed on <u>07 A</u>	ugust 2003 .				
2a)  This action is <b>FINAL</b> . 2b)  Thi	s action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. Disposition of Claims					
4)⊠ Claim(s) <u>1,44-52 and 57-59</u> is/are pending in t	he application.				
4a) Of the above claim(s) is/are withdrawn from consideration.					
5) Claim(s) is/are allowed.					
6)⊠ Claim(s) <u>1,44-52 and 57-59</u> is/are rejected.					
7) Claim(s) is/are objected to.					
8) Claim(s) are subject to restriction and/or Application Papers	election requirement.				
9) The specification is objected to by the Examiner	`.				
10)⊠ The drawing(s) filed on <u>21 December 2001</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.					
Applicant may not request that any objection to the	drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).			
11)☐ The proposed drawing correction filed on	is: a)  approved b)  disappro	eved by the Examiner.			
If approved, corrected drawings are required in rep	ly to this Office action.				
12)☐ The oath or declaration is objected to by the Exa	aminer.				
Priority under 35 U.S.C. §§ 119 and 120					
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a) ☐ All b) ☐ Some * c) ☐ None of:					
<ol> <li>Certified copies of the priority documents</li> </ol>	s have been received.				
2. Certified copies of the priority documents	s have been received in Applicati	on No			
<ul> <li>Copies of the certified copies of the prior application from the International Bur</li> <li>See the attached detailed Office action for a list of the certified of the copies of the prior application.</li> </ul>	eau (PCT Rule 17.2(a)).	•			
14) Acknowledgment is made of a claim for domestic	priority under 35 U.S.C. § 119(e	e) (to a provisional application).			
a) ☐ The translation of the foreign language pro	• •				
Attachment(s)	, , ,				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s)	5) Notice of Informal I	(PTO-413) Paper No(s) Patent Application (PTO-152)			

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Claims 2-43 have been cancelled as instructed in preliminary amendments filed on 12/21/2001 and 5/24/2002. In response to the restriction requirement, Applicants elect with traverse Group I, claims 1 and 44-52 for examination. Non-elected claims 53-56 have been cancelled. Newly submitted claims 57-59 have been entered. The active claims are 1, 44-52 and 57-59.

Claims 1, 44-52 and 57-59 are rejected under the judicially created doctrine of double patenting over claims 1-15 of U. S. Patent No. 6,338,129 since the claims, if allowed, would improperly extend the "right to exclude" already granted in the patent.

The subject matter claimed in the instant application is fully disclosed in the patent and is covered by the patent since the patent and the application are claiming common subject matter, as follows: an array processor having clusters of processing elements connected by cluster switches in torus manner.

Furthermore, there is no apparent reason why applicant was prevented from presenting claims corresponding to those of the instant application during prosecution of the application which matured into a patent. See *In re Schneller*, 397 F.2d 350, 158 USPQ 210 (CCPA 1968). See also MPEP § 804.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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Claims 1, 44-52 and 57-59 are rejected under 35 U.S.C. 102(a) as being clearly anticipated by Applicants' admitted prior art.

See Figures 1 and 2 and the corresponding description in the specification. The drawings show an array processor having a plurality of processing elements arranged in clusters and interconnected by cluster switches in torus direction.

Claims 1, 44-52 and 57-59 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Barker (USP 5,717,943).

See Figures 6, 10 and the corresponding description in lines 7-18 of column 26, claims 113 and 117 in Barker. Barker teaches an array processor having a plurality of processing elements arranged in clusters and interconnected by cluster switches in torus direction.

DAVID Y. ENG PRIMARY EXAMINER